Systems Programming in Concurrent Prolog

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Abstract

Concurrent Prolog [28] combines the logic programming computation model with guarded-command indeterminacy and dataflow synchronization. It will form the basis of the Kernel Language [21] of the Parallel Inference Machine [36], planned by Japan's Fifth Generation Computers Project. This paper explores the feasibility of programming such a machine solely in Concurrent Prolog (in the absence of a lower-level mogramming language), by implementing in it a representative collection of systems programming problems.

1. Introduction

The process of turning a bare von Neumann machine into a usable computer is well understood. One of the more elegant techniques to do so is to implement a cross-compiler for a systems programming language (say C) on a usable computer. Then implement in that language an operating system kernel (say Unix), device drivers, a file system, and a programming environment. Then boot the operating system on the target computer. From that stage on the computer is usable, and application programs, compilers and interpreters for higher-level languages (say Franz Lisp and CProlog) can be developed on it.

This paper addresses the question of turning a bare computer into a usable one, but for a machine of a different type, namely, a parallel logic programming machine. In particular, it explores the suitability of Concurrent Prolog [28] as the kernel programming language of such a computer, by asking the question:

 Will a machine that implements Concurrent Prolog in hardware or firmware be usable as a general-purpose, multi-user, interactive computer?

or, stated slightly differently,

Is Concurrent Prolog expressive enough to be a kernel language of a general-purpose computer?

We investigate there questions only from the software, not from the hardware, side. Our concern with obliding is limited by the assumption that the machine will execute at least as many Megalips² as today's computers execute Mips. ⁷

These questions are far reaching, but not purely speculative, given the Fifth Generation Computers Project's plans to design and build a parallel logic programming machine, and to use Concurrent Prolog as the basis for the machine's kernel language [21].

The paper attempts to give some evidence towards affirmative answers to these questions. To do so, we assume a computer that behaves like a virtual Concurrent Prolog machine, but has no other (lower-level or otherwise) programming constructs, special instructions, hardware interrupts, etc. We also assume that basic drivers for I/O specification and debugging system [33], and a LOOPS-like [2] object-oriented innowledge representationlanguage [9].

2. Concurrent Prolog

Concurrent Prolog is a logic programming language, in that a program is a collection of universally quantified Horn-clause axioms, and a computation is an attempt to prove a goal—an existentially quantified conjunctive statement—from the axioms in the program. The goal statement describes an input/output relation for which the input is known; a successful (constructive) proof provides a corresponding output.

The difference between Concurrent Prolog and other logic programming languages (e.g. pure Prolog) is in the mechanism they provide for controlling the construction of the proof. Prolog uses the order of clauses in the program and the order of goals in a clause to guide a sequential search for a proof, and uses the cut operator to prune undesired portions of the search space. Concurrent Prolog searches for a proof in parallel. To control the search. Concurrent Prolog embodies two familiar concepts:

¹The term Kernel Language denotes a hybrid between a machine language and a systems programming language, implemented by hardware or firmware. As far as I know it was introduced by the Fifth Generation Project [23].

²LIPS: Logical Inferences Per Second. In the context of Concurrent Prolog, it means process reductions per second.

gnarded-command indeterminacy, and dataflow synchronization. They are implemented using two constructs: the commit operator "|" and the read-only annotation "?".

The commit operator is similar to Dijkstra's guarded command [7], and was first introduced to logic programming by Clerk and Gregory [4]. It allows a process to make preliminary computations (specified in the guard of a ciause), before choosing which action to take, i.e. which clause to use for reduction. Read-only amountations on occurrences of variables are the basic (and only) mechanism for process synchronization. Roughly speaking, a process that attempts to instantiate a variable through a read-only occurrence of it suspends until the variable is instantiated by another process. The other components of concurrent programming: process creation, termination, and communication, are already available in the abstract computation model of logic programming. A unit goal corresponds to a process, and a conjunctive goal to a system of processes. Approximate is created via goal reduction, and terminated by being reduced to the empty (true) goal. Conjunctive goals may share variables, which are used as communication channels between processes.

More precisely, a Concurrent Prolog program is a finite set of guarded-clauses. A guarded-clause is a universally quantified axiom of the form

$$A \leftarrow G_1, G_2, \dots, G_m \mid B_1, B_2, \dots, B_n, m, n \geq 0.$$

where the G's and the B's are atomic formulas, also called unit goals. A is called the clause's head, the G's are called its guard, and the B's its body. When the guard is empty the commit operator " $\|$ " is omitted. Clauses may contain variables marked read-only, such as "X". The Edinburgh Prolog syntactic conventions are followed: constants begin with a lower-case letter, and variables with an upper-case letter. The special binary term $[X \mid Y]$ is used to denote the list whose head (car) is X and tail (cdr) is Y. The constant $\|$ denotes the empty list.

Concerning the declarative semantics of a guarded clause, the commit operator reads like a conjunction: A is implied by the G's and the B's. The read-only annotations can be ignored in the declarative reading.

Procedurally, a guarded-clause specifies a behavior similar to an alternative in a guarded-command. To reduce a process A using a clause

$$A1 \leftarrow G \mid B$$
,

unify A with A1, and, if successful, recursively reduce G to the empty system, and, if successful, commit to that clause, and, if successful, reduce A to B.

The unification of a process against the head of a clause serves several functions: passing parameters, assigning values to variables, selecting and constructing datastructures, and sending and receiving messages. The example programs below demonstrate all these uses of unification. The reduction of a process may suspend or fail during almost any of the steps described above. The unification of the process against the head of a clause suspends if it requires the instantiation of variables occurring as read-only in $A_i^{\prime\prime}$ It fails if A and AI are not unifiable. The computation of the guard system G suspends if any of the processes in it suspends, and fails if any of them fails. As in guarded-commands, at most one of the process's or-parallel guard-systems may commit.

Prior to commitment, partial results computed by the first two steps of the reduction—unifying the process against the head of the clause and solving the guard—are not accessible to other processes in A's system. This prevents interference between brother or-parallel computations, and eliminates the need for distributed backtracking.

This completes the informal description of Concurrent Prolog. The simplicity of the language is an asset when attempting a hardware or firmware implementation of it.

3. A Meta-Interpreter for Concurrent Prolog

One of the simpler ways to implement a programming environment for a programming language L is augmenting L's interpreter. Among the program development tools that can be implemented in this way are sophisticated debuggers [27], runtimestatistics packages,—extensions to the language, and new embedded languages. The difficulty of implementing these tools grows with the complexity of that interpreter.

For reasons of bootstrapping and elegance, the preferred implementation language for L's programming environment is Litself, as argued eloquently by Sandewell[25]. Hence the ease in which an L interpreter can be implemented in L is of clear practical importance, as well as a useful criteria for evaluating the expressiveness and completeness of the language, as argued by Sussman and Steele in [32].

Designing an expressive language with a simple meta-interpreter³ is like solving a fixpoint equation. If the language L is too weak, then L's data-structures may not be rich enough to represent L programs conveniently. If the control constructs of L are incomplete they cannot be used to simulate themselves conveniently.

On the other hand, if the control structures of L are awkward and unrestricted and the data-structures are too baroque, then its interpreter becomes very large and unintelligible (e.g. goto cannot be used in a simple way to simulate unrestricted goto, but the easiest way to simulate a while statement is using a while statement in the interpreter).

A meta interpreter for pure sequential Prolog can be written in three Prolog clauses, and, indeed, implementing software tools and embedded languages via extending this interpreter is a common activity for Prolog programmers.

A meta interpreter for Concurrent Prolog is described below. It assumes the existence of a built-in system predicate clauses (A, Cs), that returns in Cs the list of all clauses in the interpreted program whose head is potentially unifiable with A.⁴ The constant true significs an empty guard or an empty body.

```
reduce(true).

reduce((A,B)) ←

reduce(A²), reduce(B²).

reduce(A) ←

clauses(A,Clauses) |

resolve(A,Clauses,Body),

reduce(Body?).

resolve(A,[(A←Guard|Body)|Cs],Body) ←

reduce(Guard) | true.

resolve(A,[C|Clauses],Body) ←

resolve(A,Clauses,Body) | true.
```

Program J: A Meta-interpreter for Concurrent Prolog

Like any other Concurrent Prolog program, Program 1 can be read both declaratively, i.e. as a set of axioms, and operationally, i.e. as a set of rules defining the behavior of processes. Declaratively, reduce(A) states that A is true (provable) with respect to the axioms defined in the predicate clauses. Operationally, the process reduce(A) attempts to reduce the system of processes A to the empty (halting) system true.

Declaratively, the axioms of reduce read: true is true. The conjunction A, B is true if A is true and B is true. The goal A is true if there are clauses Cs with the same head predicate of A, resciving A with Cs gives B, and B is true. The predicate resolve $(A, |G||G_2)$ reads, declaratively, that resolving A with the axioms $[C|G_3]$ gives B if the clause C has head A, guard G and body B, and the guard G is true, or if recursively resolving A with Cs gives B.

Operationally, the clauses of reduce say that the process true halts. The process (A, B) reduces itself to the processes A and B, and that the process A, with clauses Cs, reduces itself to B if the result of resolving A with Cs is B.

³Called a meta-circular interpreter in [32].

⁴In our current implementation Cs is the list of all clauses with the same head predicate as A. Better indexing mechanism can make the predicate more selective. Another possible optimization is to use the bounded-buffer technique of Takeuchi and Furukawa [34], to generate clauses on a demand-driven basis.

The reader not familiar with logic-programming may be puzzled by this interpreter. It seems to capture the control part of the computation, but does not seem to deal at all with unification, the data component. The answer to the puzzle is that the call to the first clause of resolve is doing the work, by unifying the process with the head of the clause. Their unification is achieved by calling them with the same name, A.

This interpreter assumes one global program, whose axioms are accessible via the system predicate clauses, as in conventional Prolog implementations. In a real implementation of Concurrent Prolog, programs would be objects that can be passed as arguments, and reduce and clauses would have an additional argument, the program being simulated.

This interpreter cannot execute Concurrent Prolog programs that use built-in system predicates, such as itself (it uses the predicate clauses). The current implementation of Concurrent Prolog contains several (13) system predicates: metalogical predicates (clauses and system), control predicates (otherwise and =), interface to the underlying prolog, I/O (read and write), and arithmetic predicates (the lazy evaluator := and 5 arithmetic test predicates). To handle system predicates, the interpreter can be augmented with the clause

$$reduce(A) \leftarrow system(A) \mid A.$$

system(X) is a system predicate that succeeds if X is a Concurrent Prolog system predicate, and fails otherwise. For example, the call system(system(X)) succeeds. The clause demonstrates the use of the meta-variable, a facility also available in Prolog, which allows to pass processes to other processes as data-structures. It is used extensively in the shell programs below.

One may suggest that using the metavariable facility, a Concurrent Prolog meta-interpreter can be implemented via the clause

$$reduce(A) \leftarrow A$$
.

This claim is true, except that it will be rather difficult to implement the software tools mentioned earlier as an extensions to this interpreter, whereas implementing a Concurrent Prolog single stepper by extending Program 1 is a trivial matter.

The interpreter in Program 1 is 10 to 20 times slower than the underlying Concurrent Prolog implementation. We feel that it is reasonable to pay a 10-fold slowdown during the program development phase for a good programming environment. Besides, a default to the underlying Concurrent Prolog can be incorporated easily, as in the case of system predicates, so that in developing large systems only the portion of the code that is under development needs the extra layer of simulation.

4. Streams

Concurrent Prolog processes communicate via shared logical-variables. Logical variables are single-assignment: they can be either uninstantiated or instantiated, but, once instantiated, their value cannot be destructively modified. Hence the Concurrent Prolog computation model is indifferent to the distinction between the shared-memory computation model and the communication based model. A shared logical-variable can be viewed as a shared memory cell that can accept only one value, or as a communication channel that can transmit only one message.

The distinction between the "reader" and "writer" of a shared variable (or the "sender" and "receiver" of the message) is done via read-only annotations. A process $p(\ldots X?\ldots)$ cannot instantiate X. Attempts of p to reduce itself to other processes using clauses that require the instantiation of X, such as

$$p(...f(a)...) \leftarrow ...$$

suspend, until X is instantiated by some other process. If X is instantiated to f(Y), then the process p can unify with that clause, even though it instantiated Y to a, since the scope of a read-only annotation is only the main functor of a term, but not variables that occur inside the term. This property enables a powerful programming technique that uses incomplete messages [28].

Even though logical variables are single-assignment, two processes can communicate with each other via a single shared variable, by instantiating a variable into a term that contains both the message and another variable, to be used in subsequent communications. This programming techniques gives the effect of streams.

The cleanest way to implement I/O functions in a Concurrent Prolog machine is for I/O devices to generate and/or consume Concurrent Prolog streams. The current implementation of Concurrent Prolog, which is an interpreter written in Prolog [28], supports only terminal I/O (the rest is done by the underlying Prolog). It implements the stream abstraction for the user terminal via two predicates, instream(X), which generated the stream X of terms typed in by the user, and outstream(X), that outputs to the screen the stream X. They are implemented using the underlying Prolog read and write predicates.

```
instream([X|Xs]) \leftarrow read(X) \mid instream(Xs).
outstream([]).
outstream([X|Xs]) \leftarrow write(X), outstream(Xs?).
```

Program 2: Implementing terminal I/O streams using read and write

If we want instream to allow the user to signify the end of the stream, the program has to be complicated a little.

Using these programs, a "device-driver" that implements a stream interface to the terminal can be specified:

```
termine!(Keyboard,Screen) ← instream(KeyBoard), outstream(Screen?).
```

In a virtual Concurrent Prolog machine in which interfaces to I/O device drivers are implemented as streams, there will be no need for specialized I/O primitives. One possible exception is a screen-output primitive (write or bitble), which may be needed for convenience and efficiency.

5. Booting an Operating System

Assume that device drivers for a terminal (screen, keyboard and a mouse), disk, and a local network have been defined for a personal workstation. Then the following program can be used to boot its operating system:⁵

```
monitor(KeyBoard?, Mouse?. Screen, DiskIn?, DiskOut, NetIn?, Netout),
terminal(KeyBoard, Mouse, Screen?),
disk(DiskIn, DiskOut?),
net(NetIn, NetOut?) |
true.

boot -
otherwise |
boot.
```

Program 2: Booting an operating system

The first clause invokes the device drivers and the monitor. The second clause automatically reboots the system upon a software crash of either the monitor or the device drivers. etherwise is a Concurrent Prolog system predicate that succeeds if and when all of its brother or-parallel guards fail. Declaratively, it may read as the negation of the disjunction of the guards of the brother clauses.

We are aware of the fact that efficiency considerations may prevent the use of pure streams for devices that generate a lot of useless data, such as a mouse, and that some lower-level interface may be required.

⁶The predicate otherwise is not implemented correctly in the current Concurrent Prolog interpreter [28]. It may succeed when it has suspended brother or-parallel guards, instead of suspending, and succeeding only when all such guards fail. Hence programs using it are not fully debugged.

Note that foreground processes are executed in the shell's guard. This allows a simple extension to shell so it will handle an abort ("control-C" on decent computers) interrupt for foreground processes. Upon the reception of an abort command the currently running foreground process (if there is one) is aborted, and the content of the input stream past the abort command is flushed. This is achieved by the clauses in Program 4a.

```
(4) shell(Xs) \leftarrow seek(abort,Xs,Ys) \mid shell(Ys?).

seek(X,[X|Xs],Xs).

seek(X,[Y|Xs],Ys) \leftarrow X = Y \mid seek(X,Xs?,Ys).
```

Program 42: An extension to the shell that handled an abort interrupt.

The program operates as follows. When an fg(X) command is received, the two guards, envelope and seek are spawned in parallel, and begin to race. The first to commit aborts the second, so if envelope terminates before seek found an abort command in the input stream (most probably because the user hasn't typed such a command yet) then the envelope commits, seek is aborted, and shell proceeds normally with the next command. On the other hand, if seek succeeds in finding an evert command before envelope terminates, then envelope is aborted, and shell proceeds with the input past the elect command, as returned by seek.

A more general interrupt, grand_abort, that aborts all processes spawned by shell, both foreground and background, can also be implemented quite easily:

```
topshell(Xs) \leftarrow shell(Xs) | true.

topshell(Xs) \leftarrow seck(grand_abort,Xs,Ys) | topshell(Ys?).
```

The distinction the shell in Program 4 makes between background and foreground processing is not of much use, however, since foreground processes are not interactive, i.e. they do not have access to the shell's input stream. One problem with the shell giving a user program its input stream is that upon termination the user program has to return the remaining stream back, so that the shell can proceed. Since we cannot expect every interactive user program to obey a certain convention for halting (cf. quit, exit, halt, stop, bye, etc.) the shell has to implement a uniform command, say exit to "softly" terminate an interactive session with a user program (in contrast to aborting it). A filter, called switch monitors the input stream to the program. Upon the reception of an exit command it closes the output stream to the program, returns the rest of the input stream to the shell, and terminates. A reasonable interactive user program should terminate upon encountering the end of the input stream. If it is not reasonable, an abort

6. A. Unix-like Shell

A shell is a process that receives a stream of commands from the terminal and executes them. In our context the commands are processes, and executing them means invoking them. A simple shell can be implemented using the metavariable facility,

shell(
$$[X|Xs]$$
) \leftarrow
 X , shell(Xs ?).
shell($[]$).

This shell is batch-oriented. It behaves like a Unix-shell that executes all commands in "background" mode, in the sense that it does not wait for the completion of the previous process before accepting the next command. As is, it achieves the effect of Unix-like pipes, using conjunctive goals with shared variables as commands. For example, the Unix command

can be simulated with the conjunctive system

provided that the Unix command p does not read from its primary input and q does not write to its primary output. External I/O by user programs is handled below.

Note that since the process's I/O streams have explicit names, we are not confined to linear pipelining, and any desired I/O configuration of the processes can be specified.

One of this shell's drawbacks is that it will crash if the user process X crashes, since X and shell(Xs) are part of the same conjunctive system, which fails if one of its members fails. This can be remedied by calling envelope(X) instead of X.

```
envelope(X) \leftarrow X | write(halted(X)).
envelope(X) \leftarrow otherwise | write(failed(X)).
```

It is easy to augment the shell to distinguish between background and foreground processes, assuming that every command X is tagged bg(X) or fg(X), as done in Program 4.

```
    (1) shell([]).
    (2) shell([fg(X)[Xs]) ←
        envelope(X) | shell(Xs?).
    (3) shell([bg(X)[Xs]) ←
        envelope(X), shell(Xs?).
```

Program 4: A shell that handles foreground and background processes

interrupt will always do the job. The following code implements this idea. Commands to interactive foreground processes are of the form fg(P,Pi), where P is the process and Pi is its input stream. For example, a command to run the process foo(X) with input stream X will be given as fg(foo(X?),X).

Program 4b: An extension to the shell that handles interactive user programs.

7. A manager of Multiple Interactive Processes

The shell described above can handle only one interactive process at a time, like the DEC supplied TOPS-30 ENEC. MUF (Multiple User Forks) is a popular DEC-20 program, developed at Yale university [6], which overcomes this limitation. It can handle multiple interactive processes, and has a mechanism for easy context switching, it cannot compete, of course, with the convenience of a system with a bitmap display and a pointing device.

MUF associates names with processes. It has commands for creating a new process, freezing or killing a process, resuming a frozen process, and others. Program 5 achieves some of this functionality.

```
(0) muf(X) ←
    muf(X,[]).
(1) muf([create(Pname,Process,Pin,Pout)|Input],Ps) ←
    Process,
    tag(Pname,Pout),
    muf([resume(Pname)|Input?],[(Pname,Pin)|Ps]).
(2) muf([resume(Pname)|Input],Ps) ←
    find_process(Pname,Ps,Pin,Ps1)|
    distribute(Input?,Pin,Input1,Pin1),
    muf(Input1?,[(Pname,Pin1)|Ps1]).
(3) muf([exit|Input],[(Pname,[])|Ps]) ←
    muf(Input?,Ps).
(4) muf([],Ps) ←
    close_input(Ps).
```

- (1) find_process(Pname,[(Pname,Pin)|Ps],Pin,Ps).
- (2) find_process(Pname,[Pr[Ps],Pin,[Pr[Ps1]) -otherwise |
 find_process(Pname,Ps,Pin,Ps1).
- distribute([],Pin,[],Pin).
- (2) distribute([X|Input],Pin,[X| Input],Pin)← muf_command(X)| true.
- (3) distribute([X|Input],[X|Pin],Input1,Pin1)← otherwise | distribute(Input?,Pin,Input1,Pin1).
- (1) close_input([]).
- (2) close_input([(Pname,[])|Ps])← close_input(Ps).
- muf_command(create(_,_,_,_)).
- (2) muf_command(resume(_)).
- (3) muf_command(exit).

Program 5: mini-MUF

The muf process is invoked with the call muf(X?) where X is its input stream. It first initializes itself with the empty process list, using Clause (0), then iterates, serving user commands.

On the command create(Pname, Process, Pi, Po) it creates a process Process, and a process tag(Pname, Po), that tags the process's output stream elements with the process's name, and displays them on the screen. It also adds a record with the process's name, Pname, and input stream, Pi, to its process list, and sends itself the command resume(Pname).

On the command resume(Pname), muf uses Clause (3). It searches its process list for the input stream of the process Pname, and puts this process record first on the list. This is done by find_process. If successful, it invokes distribute(Input?, Pin, Input1, Pin1), which copies the elements of the stream Input to the stream Pi (Clause 3) until it reaches the end of the stream (Clause 1), or encounters a command to muf (Clause 2). In that event it terminates, returning the updated streams in Pi1 and Input1. muf itself is suspended on Input1.

On the command exit, muf closes the input stream of the current process, and removes it from the process list (Clause 3).

When encountering the end of its input stream, muf closes the input streams of all the processes in its list, and terminates (Clause 4).

Some of the frills of the real MUF can be easily incorporated in our mini-

implementation. For example, the *freeze* command resumes the previously resumed process, without having to name it explicitly. This is implemented by the following clause:

```
\begin{array}{l} muf([freeze|Input],[Pr,(Pname,Pi)|Ps]) \leftarrow \\ muf([resume(Pname)|Input?],[(Pname,Pi),Pr|Ps]). \end{array}
```

which reverses the order of the first two process records on the process list, and sends itself a resume command with the name of the previously resumed process. A similar default for exit can be added likewise.

Note that if the length of the process list is less then two, this clause would not apply, since its head would not unify with the *muf* process. Similarly, if a resume command is given with a wrong argument, Clause (2) wouldn't apply, since the guard, find_process, would fail.

muf, as defined in Program 5, would crash upon receiving such erroneous commands. Adding the following clause would cause it to default, in such cases, to au error-message routine:

```
muf([X|Input],Ps) ←
otherwise |
muf_error(X,Ps),
muf(Input?,Ps).
```

muf_error analyses the command with respect to the process list, and reports to the user the type of error it's made.

Similarly easy to implement are queries concerning the names of the processes in the process list, and the identity of the currently resumed process.

8. Merging streams

A Concurrent Prolog process can have several input and/or output streams, and use them to communicate with several other processes; but the number of these streams is fixed for any given process. It is sometimes convenient to determine or change at runtime the number of processes communicating with another process; this can be achieved by merging communication streams.

In some functional and dataflow languages merge is a built in operator [1,11]. Logic programs, on the other hand, can express it directly, as shown by Clark and Gregory [4]. Program 6 adapts their implementation to Concurrent Prolog. It imple-

ments the process merge(X?, Y?, Z), which computes the relation "Z is the interleaving of X and Y".

```
merge([X|Xs], Ys, [X|Zs]) \leftarrow merge(Xs?, Ys, Zs).

merge(Xs, [Y|Ys], [Y|Zs]) \leftarrow merge(Xs, Ys?, Zs).

merge(Xs,[], Xs).

merge([], Ys, Ys).
```

Program 6: Merging two streams

Using stream-merge as the basic method of many-to-one communication poses three major problems:

- 1. How to provide a fair access to the shared process?
- 2. How to minimize communication delay?
- 3. How to route a response back to the sender?

The building-block of a fair communication network is a fair merge operator. The abstract computation model of Concurrent Prolog is under-specified, and does not determine which of the first two clauses of Program 6 would be chosen for reduction if both input streams have elements ready. Dijkstra ([7], p. 204) has considered this underspecification a desirable property of the guarded-command, and recommended simulating a totally erratic demon when choosing between two applicable guards. Nevertheless, for reasons of efficiency and expressiveness, we prefer to work in a more stable environment, and allow the programmer to control the chosen clause in the special case in which there are applicable clauses with empty guards.

A stable Concurrent Prolog machine always reduces a process using the first unifiable clause with an empty guard, if such a clause exists.

A stable implementation is a natural consequence of having a sequential dispatcher for the guards of a process. Such a dispatcher would perform the unification of the process against the clauses' heads sequentially, and dispatch the guard of a clause if the unification with its head succeeds. It would commit as soon as it succeeds in unifying the process with the head of a clause whose guard is empty.

The definition of a stable machine assumes that some order (say, text order) is imposed on the clauses of each procedure in the program. Note that a stable implementation guarantees nothing about the selection of clauses with non-empty guards.

On a stable machine, Program 6 above will always prefer the first stream over the second, if both streams have elements ready. Hence it does not guarantee bounded waiting (however, it may be used to implement a notion of interrupts with different relative priorities).

To achieve fairness, this program is modified slightly, so it switches the positions of the two streams on each reduction, as specified in Program 7. On a stable machine, this would ensure 2-bounded-waiting.

```
merge([[X|Xs], Ys, [X|Zs]) \leftarrow merge(Ys, Xs?, Zs).

merge(Xs, [Y|Ys], [Y|Zs]) \leftarrow merge(Ys?, Xs, Zs).

merge(Xs,[], Xs).

merge([], Ys, Ys).
```

Program 7: Fairly merging two streams

This program is a satisfactory solution to the problem of merging two streams. More than two streams can be merged by constructing a tree of merge operators. It is not difficult to see (cf. [29])that a balanced merge tree composed of fair binary merge operators ensures linear bounded-waiting, and has a logarithmic communication delay.

The construction of a static balanced merge tree is easy. To allow a dynamically changing set of processes a fair and efficient access to a shared resource, a more innovative solution is required. In [29]we define self-balancing binary and ternary merge operators. These operators compose dynamically into a balanced merge-tree, using algorithms similar to 2-3-tree insertion and deletion. The algorithms require sending messages that contain communication channels, in order to reshape the tree. In other words, the algorithm uses incomplete messages. 2-3 merge trees also ensure linear bounded-waiting and logarithmic communication delay, hence we believe they provide an acceptable solution to the problem of dynamic many-to-one communication.

The problem of routing back the response to a message is solved, at the programming level, using incomplete messages. A message that requires a response typically contains an uninstantiated variable; the sender of the message suspends, looking at the variable in read-only mode. The recipient of the message responds to it by instantiating that variable. This technique is used in the monitor, queue, and disk scheduling programs below.

8.1 A note on abstract stream operations

A more abstract (but also longer and less efficient) implementation of merge can be obtained using the send(X,S,S1) and receive(X,S,S1) operations on streams. They

define the relation "the result of sending (receiving) X on stream S is the stream S1" as follows:

```
send(X,[X|Xs],Xs).

receive(X,[X|Xs],Xs?).
```

Such an implementation hides the internal representation of the stream, and eliminates the need to use the read-only annotation almost entirely in the calling program, since the resulting stream of receive is already annotated as read-only. We find the use of send and receive explicitly, instead of achieving this effect implicitly via unification, essential for the readability of programs with complex communication patterns, such as the ones described in [15,26].

The send and receive calls can be eliminated for the sake of efficiency using standard partial-evaluation and program-transformation techniques [20,35].

9. Monitors and the readers-and-writers problem

The Concurrent Prolog solution to the readers and writers problem uses this method of many-to-one communication. It is very similar, in spirit, to the idea of monitors [16]. A designated process (a 'monitor') holds the shared data in a local argument, and serves the merged input stream of 'read' and 'write' requests ('monitor calls'). It responds to a 'read' request through the uninstantiated response variable in it ('result argument').

A schematic implementation of a monitor is shown in Program 8. Note that it serves a sequence of read requests in parallel, since the recursive invocation of monitor in Clause (2) is not suspended on the result of serve, in contrast to Clause (1).

```
    monitor([write(Args)|S], Data) ←
        serve(write(Args), Data, NewData), monitor(S?, NewData?).
    monitor([read(Args)|S], Data) ←
```

serve(read(Args), Data, _), monitor(S?, Data).

(3) monitor([],_).

Program 3: A schematic implementation of a monitor

In monitor-based programming languages, a procedure call and a monitor call are two basic, mutually irreducible operations. In Concurrent Prolog, on the other hand, there is one basic construct, a process invocation, whereas a monitor call is a secondary concept, or, rather, a programming technique.

Concurrent Prolog monitors and merge operators can implement operating systems in a functional style without side-effects, using techniques similar to Henderson's [11].

10. Queues

Merged streams allow many client processes to share one resource; but when several client processes want to share several resources effectively, a more complex buffering strategy is needed. Such buffering can be obtained with a simple FIFO queue: a client who requires the service of a resource enqueues its request. When a resource becomes available it dequeues the next request from the queue and serves it.

The following implementation of shared queues is a canonical example of Concurrent Prolog programming style. It exploits two powerful logic programming techniques: incomplete messages, and difference-lists.

A shared queue manager is an instance of a monitor. enqueue is a "write' operation, and "dequeue" involves both "read" and "write". An abstract implementation of a queue monitor is shown in Program 9.

- (0) queue_monitor(S) ←create_queue(Q), queue_monitor(S,Q).
- queue_monitor([Request|S],Q) ← serve(Request,Q,Q1), queue_monitor(S?,Q1?).
- (2) queue_monitor([],Q).

Program 9: A queue monitor

Clause (0) creates an empty queue; Clause (1) iterates, serving queue requests; and Clause (2) halts the queue monitor upon reaching the end of the requests stream.

The implementation of the queue operations employs difference-lists [3]. A difference-list represents a list of elements (in this context, the queue's content) as the difference between two lists, or streams. For example, the difference between [1,2,3,4|X] and X is the list [1,2,3,4]. As a notational convention, we use the binary term X\Y (read "the difference between X and Y"), to denote the list that is the difference between the list X and the list Y. Note that this term has no special properties predefiend, and any binary term will do, as long as it is used consistently.

 $\label{eq:create_queue} $$\operatorname{create_queue}(X\setminus X).$$ serve(enqueue(X), Head X|NewTail), Head NewTail).$$ serve(dequeue(X), X|NewHead Tail).$

Program 9a: Queue operations

create_queue(Q) states that Q is an empty difference-list. The clauses for serve define the relation between the operation, the old queue, and the new queue. On an enqueue(X) message, X is unified with the first element of the Tail stream, and in the new queue NewTail is the rest of the stream. On a dequeue(X) message, X is unified with the first element of the Head stream, and in the new queue NewHead is the rest of the old Head stream.

Operationally, the program mimics the pointer twiddling of a conventional queue program. One difference is the simplicity and uniformity of the way in which variables are transmitted into and from the queue, using unification, compared to any other method of parameter passing and message routing.

Another is the behavior of the program when more dequeue messages have arrived then enqueue messages. In this case the content of the difference-list becomes "negative". The Head runs ahead of the Tail, and the negative difference between them is a list of uninstantiated variables, each for an excessive dequeue message. Presumably, a process who sends such a message then suspends on its variables in a read-only mode. One consequence is that excessive dequeue requests are served exactly in the order in which they arrived.

The program can be condensed and simplified, using program transformation techniques [20,35]. The resulting program is more efficient, and reveals more clearly the declarative semantics of the queue monitor. Its operational semantics, however, seems to become a bit more obscure, and its does not hide the internal representation of the queue, as Program 10 does.

 queue_monitor(S) ← queue_monitor(S?, X\X).

 queue_monitor([dequeue(X)|S], [X|NewHead\Tail) ← queue_monitor(S?, NewHead\Tail).

(2) queue_monitor([enqueue(X)|S], Head\[X[NewTail]) ← queue_monitor(S?, Head\NewTail).

(3) queue_monitor([],_).

Program 10: A simplified queue monitor

Declaratively, the queue_monitor program computes the relation queue(S), which says that S is a legal stream of queue operations. It uses an auxiliary relation $queue_monitor(S,Dequeue Enqueue)$, which says that Dequeue is the list of all elements X such that dequeue(X) occurs in S (Clause 1), and that Enqueue is the list of all elements X such that enqueue(X) occurs in S (Clause 2). The interface between these two relations (Clause 0), constrains the list of enqueued elements to be identical to the list of dequeued elements, by calling them with the same name.

11. Bounded-buffer communication

Bounded buffers were introduced into logic programming by Clark and Gregory [4] as a primitive construct. Their principal use in logic-programming is not to utilize a fixed memory-area for communication, but rather to enforce tighter synchronization between the producer and the consumer of a stream.

Takeuchi and Furukawa [34]have shown how to implement bounded-buffers Concurrent Prolog, hence it need not be considered a primitive. Their implementation represents the buffer using a difference-list, and uses incomplete messages to synchronize the producer and the consumer of the stream.

12. An implementation of the SCAN disk-arm scheduling algorithm

The goal of a disk-arm scheduler is to satisfy disk I/O requests with minimal arm movements. The simplest algorithm is to serve the next I/O request which refers to the track closest to the current arm position. This algorithm may result in unbounded waiting—a disk I/O request may be postponed indefinitely. The SCAN algorithm tries to minimize the arm movement, while guaranteeing bounded waiting. The algorithm reads as follows:

"while there remain requests in the current direction, the disk arm continues to move in that direction, serving the request(s) at the nearest cylinder; if there are no pending requests in that direction (possibly because an edge of the disk surface has been encountered), the arm direction changes, and the disk arm begins its sweep across the surface in the opposite direction" (from [17]p.94).

- (0) disk_scheduler(DiskS, UserS) ← disk_scheduler(DiskS?, UserS?, ([], []), (0, up)).
- (1) disk_scheduler([Request|DiskS], UserS, Queues, ArmState) ← dequeue(Request, Queues, Queues1, ArmState, ArmState1) | disk_scheduler(DiskS?, UserS, Queues1, ArmState1).

```
(2) disk_scheduler(DiskS,[Request|UserS], Queues, ArmState) -
          enqueue(Request, Queues, Queuesi, ArmState) |
          disk_scheduler(DiskS, UserS?, Queuesi, ArmState).
(3) disk_scheduler([io(0, halt)| _], [], ([],[]), _).
(1) dequeuc(io(T,X), ([io(T,X)|UpQ],[]), (UpQ,[]), _, (T,up)).
(2) dequeue(io(T,X), ([io(T,X)|UpQ],DownQ), (UpQ,DownQ), (_,up), (T),up)).
(3) dequeue(io(T,X), (||, [io(T,X)] DownQ]), (||,DownQ), -, (T,down)).
(4) dequeuc(io(T,X), (UpQ, io(T,X)|DownQj), (UpQ,DownQ), (_,down), (T,down)).
(1) enqueue(io(T, Args), (UpQ, DownQ). ([io(T, Args)|UpQ], DownQ), (T, down)).
(2) enqueuc(io(T, Args), (UpQ, DownQ), (UpQ, lio(T, Args)|DownQ|), (T, up)).
(3) enqueuc(io(T, Args), (UpQ, DownQ), (UpQ1, DownQ), (T1, Dir)) -
          T>T1 | insert(io(T, Args), UpQ, UpQ1, up).
(4) enqueuc(io(T, Args), (UpQ, DownQ), (UpQ, DownQ1), (T1, Dir)) \(\to \)
         T<T1 | insert(io(T, Args), DownQ, DownQ1, down).
(1) insert(io(T, X), [], [io(T,X)],...).
(2) insert(io(T, X), [io(T1, X1)|Q], [io(T, X), io(T1, X1)|Q], up)\leftarrow
          T<T1 | true.
(3) insert(io(T, X), [io(T1, X1)|Q], [io(T, X), io(T1, X1)|Q], down)\leftarrow
          T>T1 | true.
(4) insert(io(T, X), [io(T1, X1)]Q], [io(T1, X1)]Q1], up) ←
          T>=T1 | insert(io(T, X), Q, Q1, up).
(5) insert(io(T, X), [io(T1, X1)|Q], [io(T1, X1)|Q1], down) \leftarrow
          T=<T1 \mid insert(io(T, X), Q, Q1, down).
```

Program *: The SCAN disk-arm scheduler

The disk scheduler has two input streams—a stream of I/O requests from the user(s) of the disk, and a stream of incomplete messages from the disk itself. The scheduler has two priority queues, represented as lists: one for requests to be served at the upsweep of the arm, and one for the requests to be served at the downsweep. It represents the arm state with the pair (Track, Direction), where Track is the current track number, and Direction is up or down.

The disk scheduler is invoked with the goal:

disk_scheduler(DiskS?, UserS?)

where UserS is a stream of I/O requests from the user(s) of the disk, and DickS is a stream of partially determined (incomplete) messages from the disk controller. I/O requests are of the form io(Track, Args), where Track is the track number and Args contain all other necessary information.

The first step of the scheduler is to initialize itself with two empty queues and the arm positioned on track 0, ready for an upsweep; this is done by Clause (0). Following the initialization, the scheduler proceeds using three clauses:

- Clause (1) handles requests from the disk. If such a request is ready in the disk stream, the scheduler tries to dequeue the next request from one of the queues. If successful, that request is unified with the disk request, and the scheduler iterates with the rest of the disk stream, the new queues, and the new arm state. The dequeue operation fails if both queues are empty.
- ▶ Clause (2) handles requests from the user. If an I/O request is received from the user it is enqueued in one of the queues, and the scheduler iterates with the rest of the user stream and the new queues.
- ▶ Clause (3) terminates the scheduler, if the end of the user stream is reached and if both queues are empty. Upon termination, the scheduler sends a 'halt' message to the disk controller.

The dequeue procedure has clauses for each of the following four cases:

- ▶ Clause (1): If DownQ is empty then it dequeues the first request in UpQ, and changes the new state to be upsweep, where the track number is the track of the I/O request.
- ▶ Clause (2): If the arm is on the upsweep and UpQ is nonempty then it dequeues the first request in UpQ. The new state is as in Clause (1).
- ▶ Clauses (3) and (4): Are the symmetric clauses for DownQ.

Note that no clause applies if both queues are empty, hence in such a case the dequeue procedure fails. Since the disk scheduler invokes dequeue as a guard, it must wait in this case for the next user request, and use Clause (3) to enqueue it. If such a request is received and enqueued then in the next iteration the disk request can be served.

The enqueue procedure also handles four cases. If the I/O request refers to the current arm track, than according to the SCAN algorithm it must be postponed to the next sweep. Clauses (1) and (2) handle this situation for the upsweep and downsweep cases. If the request refers to a track number larger than the current track, then it is inserted to UpQ by Clause (3), otherwise it is inserted to DownQ, by Clause (4).

The insertion operation is a straightforward ordered-list insertion. More efficient data-structures, such as 2-3-trees, can be used if necessary.

To test the disk scheduler, we have implemented a simulator for a 10-track disk controller. The controller sends a stream of partially determined I/O requests, and, when the arguments of the previous request become determined, it serves it and sends the next request.

Program *: A simulator of a 10-track disk controller

When invoked with a stream S, the controller initializes the disk content and sends the first request using Clause (0). It then iterates with Clause (1), serving the previous I/O request and sending the next partially determined request, until a halt message is received, upon which it closes its output stream and terminates, using Clause (2).

The disk simulator assumes that the arguments of an I/O request are pairs (Operation, Result Code), where the operations are read(X) and write(X). On read(X) Clause (2) unifies X with the content of the requested track number. On write(X) Clause (3) replaces the requested track content with X. The Result Code is unified with true if the operation completed successfully (Clauses (2) and (3)), and with false otherwise (Clause (1)). An example of an unsuccessful completion is when the requested track number exceeds the size of the disk.

13. Conclusion

We have provided some evidence that a machine that implements Concurrent Prolog in hardware or firmware will be self-contained, usable, and useful, without much need to reactionary concepts and techniques.

The next logical step is to build it.

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The Bagel: A Systolic Concurrent Prolog Nachine (Lecture Notes)

Abstract

It is argued that explicit mapping of processes to processors is essential to effectively program a general-purpose parallel computer, and, as a consequence, that the kernel language of such a computer should include a process-to-processor mapping notation.

The Bagel is a parallel architecture that combines the concepts of dataflow, graph-reduction, and systolic arrays. The Bagel's kernel language is Concurrent Prolog, augmented with Turtle programs as a mapping notation.

Concurrent Prolog, combined with Turtle programs, can easily implement systolic systems on the Bagel. Several systolic process structures are explored via programming examples, including linear pipes (sieve of erasthotenes, merge sort, natural-language interface to a database), rectangular arrays (rectangular matrix multiplication, band-matrix multiplication, dynamic programming, array relaxation), static and dynamic H-trees (divide-and-conquer, distributed database), and chaotic structures (a herd of Turtles).

All programs shown have been debugged using the Turtle graphics Bagel simulator, which is implemented in Prolog.

Keywords: parallel processing, Concurrent Prolog, logic programming, graph reduction, dataflow, systolic algorithms, Turtle geometry.

How to evaluate proposed architectures?

One criteria [Arvind]: Scalability

- For twice the money, get twice the computer,

or

 The architecture should remain feasible as the number of processors goes to infinity.

Implication of scalability:

Fon-uniform communication/memory-reference costs - (Crossbar switches and their approximations are not scalable).

Implication of non-uniform communication costs:

Ensuring the locality of communication and memory-references is crucial for efficient

parallel processing.

The Basic Question:

- How to control communication?

or

- How to ensure locality of communication?

Several answers:

- (1) "Smart" load-balancing algorithms.
- (2) "Smart" compilers.
- (3) Scart programmers and algorithm designers.

Statement:

Answer (1) is not feasible.

A notation for specifying process-to-processor mapping is required by answers (2) and (3).

Statement:

Designing efficient process structures (which localize communication) for parallel program is as difficult as designing efficient data-structures for a sequential program (cf. systelic algorithms).

Statement:

To practical system for selecting data-structures is available.

Conclusion:

Answer (2) is not feasible (in the forsecable future).

Systelic Algorithms

- Developed by Kung and colleagues at CLT, for direct implementation in VLCI.
- Combine pipelining and multiprocessing in a single framework.

- Achieve massive parallelism - Applied so far mostly to numeric problems. Example: A systolic algorithm for band-matrix multiplication (See figure). -----Statement: Multiprocessing adds a new dimension of programming, 'To run efficiently on a sequential computer, programs must control the usage of - Space and ~ Time. To run efficiently on a parallel computer, programs must control the usage of - Space, - Time, and - Communication. ----

Implication:

Algorithm designers and programmers should have control over the mapping of processes to processors.

Implication:

A programming language for a parallel computer should include a mapping notation.

Implication:

The multiprocessor's interconnection scheme must be simple, intuitive, and of general purpose, for programmers to use it effectively.

Objection:

Programming is difficult as is. Incorporating explicit control of process-to-processor mapping would make it horrendous.

Answer:

Programming in higher-level languages is easier.

There seems to be a tradeoff between the complexity of data-structures in sequential programs and communication-structures in parallel programs, so overall program complexity is preserved.

Our experience shows that the mapping component of a program is relatively small, is not difficult to specify (children's programming...), and can be debugged independently of the main algorithm.

Objection:

Explicit mapping by user is too simple-minded and rigid. Nore sophisticated and flexible mapping strategies are essential for many applications.

Answers:

- Simple is beautiful.
- Our example programs provide evidence to the contrary.
- Load-balancing algorithms may be required in a multi-user environment. However, they should be implemented by the systems hacker, not the hardware architect. Hence the kernel programming language needs a mapping notation, 2.E.D.

A Concurrent Prolog Ad

Concurrent Prolog combines the logic programming computation model with guarded-command indeterminacy and dataflow synchronization.

It is simple. It adds to pure logic programs only two control primitives:

- The commit operator (for indctorminacy).
- Read-only annotations (for synchronization).

It is expressive. Applied so far to:

- Systems programming (ICOT TR-003; POPL-84; LPS-84; A.Takeuchi and K.Furukawa, LPN-83).
- Systolic algorithms (this talk).
- Distributed algorithms
 (A.Shafrir, Weizmann TR CS83-12; L.Hellerstein, LPS-84).
- Cbject-oriented programming and constraint systems (A.Takeuchi, J. New Generation Computing 1(1)).
- Parallel parsing (H. Sirakawa, ICOT TR-008).
- Hardware specification and debugging (N.Suzuki, in Logic Programming and its Applications, Warren & van Caneghem (eds), 1984).
- Implementation of embedded languages: Or-parallel Prolog (Hirakawa et al., ICOT TR-020). Mandala: A knowledge-programming language (K.Furukawa, A.Takeuchi, and S.Kunifuji, ICCT TR-029).

It is amenable to efficient implementation (we hope...). So far has only an interpreter implemented in Prolog (ICOT TR-003).

The Eagel: A Systolic Concurrent Prolog Hachine

- Architecture:

----- .

rectangular grid of transputers with nearest-neighbor and shifted end-round torroidal interconnections.

- Programming language:

Concurrent Prolog, augmented with Turtle programs as a mapping notation.

- Major application method:

Systelic algorithms.

- Implementation state:

Software simulators written in Prolog (with Turtle graphics) and Concurrent Prolog (very slow) exist.

Programs below were debugged using the Turtle graphics simulator.

Constructing the Eagel: Step 1

(Set figure).

.Constructing the Bagel: Step 2 (See figure). The Bagel (See figure). Aspects of the Bagel's interconnection scheme - Virtual infinite two dimensional grid (programs need not know the dimensions of the Bagel). (convenient communication structure for many applications) - A path in any direction will visit every processor once, before returning to its origin. (supports even mapping). - Simple to implement in current and forthcoming technologies. - Scalable. Aspects of the Bagel's computation model - Easic computation step: Process reduction. - Synchronization: Data-flow (read-only variables). - Interprocessor communication: Packets containing: * instantiations of shared variables. # processes and their associated programs. * process control messages (success, failure). Aspects of the Dagel's transputer

Each transputer consists of:

- Reduction processor (pipelined?)
- Randon access memory
- Communication processor.
- Associative memory
- Interface to external I/O.

Possible optimizations:

- Cache
- Two-port memory.

Possible approximations (hardware simulators):

- Reduction processor and communication processor are off-the-shelf chips.
- Associative memory simulated by a hash table.

Schecatic design of the Bagel's transputer

(See Figure)

The Bagel's kernel language

Concurrent Prolog augmented with fixed-instruction Turtle programs as notation for mapping processes to processors.

Goals (processes) can be of the form Goal@TP meaning, solve the goal (execute the process) Goal at the processor specified by the Turtle program TP.

Each process, like a Turtle, has a position and a heading. The initial position and heading of a child process is inherited from its parent.

Fixed-instruction Turtle programs are a sequence of instructions of the form:

forward(Distance), back(Distance), left(Angle), right(Angle),
turn(Degree) (where Dogree is absolute heading) [i,j] (where [i,j]
is an absolute position), stay (no-op).

(currently only integer Distances and 90 degree Angles are implemented).

Emamples of process configuration schemes

```
- Sieve of Erasthotenes.
         - Bubble sort.
        - Merge sort.
        - Hatural language interface to a DB (scheme).
    Rectangular arrays:
        - Rectangular matrix multiplication.
        - Band-matrix gultiplication.
        - Dynamic programming.
        - Array relaxation.
    Static H-trees:
        - Divide-and-conquer (towers of Hanoi).
    Dynamic H-trees:
        - Distributed database (scheme).
Linear pipe: Sieve of Erasthotenes
Abstract process structure:
    primes :- integers, sift@forward.
    sift :- filter, sift@forward.
    filter :- filter.
    integers :- integers.
Concurrent Prolog Code:
    primes(J) :- integers(2,I), sift(I?,J)@forward.
    sift([P|I],[F|R1?]) :- filter(I,P,R), sift(R?,R1)@forward.
    filter([['|I],P,R) :- 0=:= N mod P | filter(I,P,R).
    filter([F[I],P,[E[R?]]) := 0 = 1 \mod P \mid filter(I,P,R).
    intcgers(E,[E:1?]) :- E1:=E+1, integers(E1,I).
Linear pipe: Bubble-sort
(Linear time and process complexity)
Abstract process structure:
```

Linear pipes:

```
bsort :-
          bfilter,
          bsort@forward.
     bsort.
     bfilter :-
          bfilter.
     bfilter.
Concurrent Prolog Code:
     bsort([X|Xs], [Y|Ys]) :-
bfilter(X, Xs?, Xs1, Y),
bsort(Xs1?, Ys)@forward.
     bsort([], []).
     bfilter(%1, [%2;%s], [%2;Ys], Y) :-
          X1<X2 | bfilter(X1, Xs?, Ys, Y).
     bfilter(X1, [X2|Xs], [X1|Xs], Y) :-
X1>=X2 { bfilter(X2, Xs?, Ys, Y).
bfilter(X, [], [], X).
Linear pipe: merge-sort
(Linear time and logarithmic process complexity)
Abstract process structure:
nsort.
psort :-
    merge_all,
    msort@forward.
merge_all.
merge_all :-
    merge2,
    merge_all.
merge2.
merge2 :-
    merge2.
Linear pipe: nerge-sort
Concurrent Prolog Code:
(l'ote: input is a list of sorted lists).
usert([], []).
meort([X], X).
proort(Xs, Zs) :-
    Xs\=[], Xs\=[_] |
Hergc_all(Xs, Ys),
Heort(Ys?, Zs)?forward.
```

```
merge_all([], []).
merge_all([X], [X]).
merge_all([X1,X2|Xs], [Y?|Ys?]) :-
    merge2(X1?, X2?, Y),
    merge_all(Xs, Ys).
merge2([], X, X).
merge2(X, [], X).
Derge2([X|Xs], [Y|Ys], [X|Zs?]) :-
    X=<Y | Derge2(Xs, [Y|Ys], Zs).
Derge2([X|Xs], [Y|Ys], [Y|Zs?]) :-</pre>
    X>Y | nerge2([X|Xs], Ys, Zs).
----
Linear pipe: Katural language interface to a
    database (scheme).
Abstract process structure:
    process :-
        morphological,
         syntax@forward(1),
         semantics@forward(2),
         pragmatics@formard(3),
         planning@forward(4).
Concurrent Prolog code (scheme):
    process(String, Query) :-
         morphological(String?, Tokens),
         syntax(Tokens, SyntaxTree)@forward(1),
         semantics(SyntaxTree?, Formula)@forward(2),
         pregnatics(Formula?, Formula1)@forward(3),
planning(Formula1?, Query)@forward(4).
Advantages:
    - Can pipelined multiple queries.
    - Code for each stage resides only in one processor.
Rectangular array: matrix multiplication (1)
(Linear time and quadratic process complexity)
Abstract process structure:
    mm :- vmGright, mmCforward.
    VE.
    va :- ip, vacforward.
    ip :- 1p.
    íp.
```

```
Concurrent Prolog code:
    mm([], _, []).
mm([X|Xs], Ys, [Z|Zs]):-
yn(X, Ys?, Z)@right, mm(Xs?, Ys, Zs)@forward.
    vm(_, [], []).
vm(Xs, [Y|Ys], [Z|Zs]) :-
   ip(Xs?, Y?, Z), vm(Xs, Ys?, Zs)@forward.
     ip(Xs, Ys, Z) :-
         ip(X3, Ys, 0, 2).
     ip([X|Xs], [Y|Ys], ZO, Z) :-
         Z1:=(X * Y)+Z0, ip(Xs?, Ys?, Z1, Z).
     ip([], [], Z, Z).
Rectangular array: matrix multiplication (2)
A variant of the previous program, that pipelines the vectors, instead of sending them as a whole.
nm([], _, []).
nm([X|Xs], Ys, [Z|Zs]):-
vm(X, Ys?, Ys1, Z)@right, nm(Xs?, Ys1?, Zs)@forward.
vm(_, [], [], []).
vm(Xs, [Y|Ys], [Y1|Ys1], [Z|Zs]) :-
    ip(Xs?, Xs1, Y?, Y1, Z), v=(Xs1?, Ys?, Ys1, Zs)@forward.
ip(Xs, Xs1, Ys, Ys1, Z) :-
     ip(Xs, Xs1, Ys, Ys1, 0, Z).
ip([X|Xs], [X|Xs1], [Y|Ys], [Y|Ys1], ZO, Z) :-
Z1:=(X = Y)+Z0, ip(Xs?, Xs1, Ys?, Ys1, Z1, Z). ip([], [], [], Z, Z).
Rectangular array: dynamic programming
(The systolic algorithm of Kung, Guibas, and Thompson)
Abstract process structure:
     table.
     table :-
         row@right.
         table forward.
     roi:
    ro:: :-
         entry,
         rew Termind.
```

```
entry.
 Rectangular array: dynamic programming
 Concurrent Prolog code:
     Input: a list of triples (0,D1,D2), where the D's
     are matrix dimensions.
     Output: (W,D1,D2), where W is the number of
    multiplications in optimal parenthesization.
     table([!'], !').
     table(Us, Min) :-
         l's\=[_] |
        row(Ws, Ws1)@right,
         table(Ws1?, Min) Oforward.
     row([_],[]).
    row([E1,U2]Hs],[V|Hs1?]) :-
        entry(11,12,11),
         row([W2|Ws], Ws1)@forward.
    entry((W1,L1,R1),(W2,L2,R2),(W,L1,R2)):-
        U:=min(W1+L18R18R2, W2+L18L28R2).
    Note how diagonal communication channels
    between table entries are created by the row
    procedure.
H-trees: A scheme for divide-and-conquer
Abstract process structure:
    htrec.
    htree :-
        htree@(left,forward),
        htree@(right, forward).
Concurrent Prolog code:
htree(0).
htree(D++) :-
    htree(D)?(left,forward(2^(D/2))),
    htree(D)@(right,forward(2^(D/2))).
l'ote: p(X↔+,...) :- ...
Is a shorthand for: p(X1,...) :- X1>0 { X:=X1-1, ...
----
```

```
H-trees: The Towers of Hanoi
Abstract process structure:
    hanoi.
    hanoi :-
        free,
        hanoi@(left,forward),
        hanoi@(right, forward).
Concurrent Prolog code:
hanci(G,From,To,(From,To)).
hanci(N++, From, To, (Before, (From, To), After)):-
   free(From, To, Free),
    hanoi(N,From,Free,Before)@(left,forward(2^(N/2))),
   hanoi(K, Free, To, After)C(right, forward(2^(11/2))).
   free(a,b,c).
   free(a,c,b).
    free(b,a,c).
    free(b,c,a).
   free(c,z,b).
   free(c,b,a).
----
Rectangular array: band-matrix multiplication
(The systolic algorithm of Kung and Leiserson,
linear time and quadratic process complexity)
Abstract process structure:
   EE :-
       opawn_isp,
        arm@forward,
        arm@right,
        mm@(forward, right, forward, left).
   nn.
   era.
   ere :-
       spawn_isp,
        armCforward.
    spawr_isp :-
        isp.
   spaur_isp :-
        forward,
        isp.
    forward.
    forward :-
       Terward.
```

```
isp :-
         isp.
     iso.
Concurrent Prolog code:
nm(D,[Ain|Asin],[Bin|Bsin],c(Clout,Cout,Crout)) :-
     spawn_isp(D,O,Cin?,Cout,Ain?,Aout,Bin?,Bout),
     arm(D, Asin?, Asout, Clim, Clout, Bout)@forward,
     arm(D, Bsin?, Bsout, Crin, Crout, Aout)@right,
     mm(D-1, Asout?, Bsout?, c(Clin, Cin, Crin))@(forward, right, forward, left).
mm(D,[],[],c([],[],[])).
5 D is the diagonal distance from the center-point x.
F V is the vertical (horizontal) distance from x's diagonal.
arm(D, Asin, Asout, Cin, Cout, Bin) :-
     arm(D, 1, Asin, Asout, Cin, Cout, Bin).
'arm(D, V,[],[],[],[],[[]],_).
arm(D, V, [Ain | Asin], [Aout | Asout], [Cin | Csin], [Cout | Csout], Bin) :-
     spawr_isp(D, V, Cin?, Cout, Ain?, Aout, Bin?, Bout),
     V1:=V+1,
    arm(D, V1, Asin?, Asout, Csin, Csout, Bout)@forward.
spawn_isp(0, V, Cin, Cout, Ain, Aout, Bin, Bout) :-
    S we are in the O's area ...
    isp(Cin, Cout, Ain, Acut, Bin, Bout).
spawn_isp(D, V, Cin, Cout, Ain, Aout, Ein, Bout) :-
    D>O | % we are in the A (or B) area...
     forward(min(D, V), Ain, Ain1, Aout, Aout1),
    isp(Cin, Cout, Ain1?, Aout1, Bin, Bout).
spawn_isp(D, V, Cin, Cout, Ain, Aout, Bin, Bout) :-
    D<0 | 5 we are in the C area...
     forward(-D, Cin?, Cin1, Cout, Cout1);
     isp(Cin1?, Cout1, Ain, Acut, Bin, Bout).
forward(0,Cin,Cin,Cout,Cout).
forward(!++,Cin,Cin2,Cout,Cout2) :-
    get_c(C,Cin,Cin1), send(C,Cout,Cout1),
    forward(1:, Cin1?, Cin2, Cout1, Cout2).
get_c(0,[],[]).
get_c(C,[C|Cs],Cs).
isp(Cin,[C1|Cout],[A|Ain],[A|Aout],[B|Bin],[E|Bout]) :-
    get_c(C,Cin,Cin1),
    C1:=C+(A # E) !
     isp(Cin1?, Cout, Ain?, Aout, Bin?, Bout).
isp(Cs,Cs,As,As,[],[]).
isp(Cs,Cs,[],[],Es,Bs).
Rectangular array: array relaxation
Abstract process structure (simplified):
```

```
relax :-
        conitor,
        matrix@forward.
    matrix :-
        vector@right,
        matrix@forward,
        merge_monitor.
    vector :-
        spawn_cell,
        vector@forward,
        merge_monitor.
    spawn_cell :-
        cell_monitor,
        cell.
    cell :-
        cell.
    cell_monitor :-
        cell_monitor.
    merge_monitor :-
        merge_monitor.
    nonitor :-
        monitor.
Rectangular array: array relaxation
Concurrent Prolog code:
relax(X,T) :-
    monitor(Honitor?, Halt),
    matrix((1,1),X,Bottom,Top,Honitor,Halt?)@forward,
    tie_vector(Bottom?),
    tic_vector(Top?).
Smatrix(Coordinates, Matrix, Nextrow channels, Final channels).
matrix(_,[],Top,Top,[],_).
matrix((I, J),[X|Xs],Botton,Top,Monitor,Halt) :-
    vector((I,J),X,Bottom,Bottom, Vuconitor, Halt)@right,
    matrix((I+1,J),Xs,Bottom1,Top,Mmonitor,Halt)@forward,
    merge_monitor(Emonitor?, Vmonitor?, Monitor).
tie_vector([]).
tic_vector([X|Xs]) :-
    tie_cell(X), tie_vector(Xs?).
vector(IJ, Xs, Bottom, Top, Ponitor, Halt) :-
    vector(IJ, Ms, Left, Right, Bottom, Top, Monitor, Halt),
    tic_cell(Left),
    tie_cell(Right).
Swector(Coord, Leftchannel, Rightchannel, Bottomchannels, Topohnls).
vector(IJ,[],Right,Pight,[],[],[],[],_).
vector((I,J),[N|Ns],Left,Fight,[Fotte: [Bo],[Top]To],Monitor,Nalt: :-
```

```
spawn_cell((I,J),X,Left,Left1,Botton,Top,Cmonitor,Halt),
    vector((I,J+1),Xs,Left1,Right,Ps,Ts,Vconitor,Halt)@forward,
    merge_monitor(Vmonitor?, Cmonitor?, Monitor).
tie_cell(c(X,X)).
spawn_cell(IJ,X,c(Lin,Out),c(Out,Rin),c(Ein,Out),c(Out,Tin),Honitor,Halt) :-
    send(X,Out,Out1) |
    cell_monitor(X,Out1?, Honitor),
    cell(IJ, Halt, Out1, Lin?, Rin?, Bin?, Tin?).
cell(IJ, halt,[],_,_,).
cell(IJ, Halt, Out,[X1!L],[X2!R],[X3|B],[X4!T]):-
    X := ((X1+X2+X3+X4) / 4) 
    send(X,Out,Out1),
    cell(IJ, Halt, Out1, L?, R?, E?, T?).
cell_monitor(X1,[X2|Xs],[halt|Ys]) :-
    X1=:=X2 | cell_monitor(X2,Xs?,Ys).
cell_monitor(X1,[X2|Xs],[continue|Ys]) :-
    X1\=X2 | cell_monitor(X2,Xs?,Ys).
cell_monitor(_,[],[]).
merge_monitor(Xs,Ys,Zs,Halt) :-
    merge_monitor(Halt, continue, Xs, Ys, Zs).
merge_monitor(halt,_,_,_,[]).
merge_monitor(!:onitor, State, [X|Xs], [Y|Ys], Zs) :-
    merge_messages(State, X, Y, Zs, State1, Zs1),
    merge_monitor(Monitor, State1, Xs?, Ys?, 2s1).
merge_monitor(Monitor, State, Xs,[], Xs).
merge_monitor(!Conitor, State,[], Xs, Xs).
merge_messages(halt,continue,Y,[continue;Zs],continue,Zs).
merge_messages(halt, X, continue, [continue | Zs], continue, Zs).
merge_messages(X, halt, halt, [halt | Zs], halt, Zs).
merge_messages(continue, X, continue, Zs, continue, Zs).
merge_nessages(continue,continue,Y,Zs,continue,Zs).
monitor([halt | Xs], halt).
monitor([continue [Xs], Monitor) :-
    monitor(%s?, Monitor).
Chaotic process: A Turtle.
Concurrent Prolog code:
turtle :-
    instream(X), turtle(X?).
turtle([]).
turtle([X [Xs]) :- turtle(Xs?) ?X.
____
```

```
Dynamic H-trees; a scheme for a distributed database
- Relations are stored in the leaves.
- Tree nodes route queries and merge responses.
- Database grows dynamically.
Abstract process structure:
root :-
    root,
    leaf.
root :-
    root.
tree :-
    650
    treeCTP.
tree:-
    even :
    tree@TP.
tree.
leaf :- leaf_split€TP.
leaf.
leaf_split :-
    tree,
    leaf & (right, forward),
    leaf@(right,forward).
Concurrent Prolog code:
root(Xs) :-
    root(0, Xs?, Ys),
    leaf(Ys?).
root(D,[split|Xs],[split(D1,stay)|Ys?]) :-
    D1:=D+1,
    root(D1, Xs?, Ys).
root(D,[],[]).
tree([split(D++,TP)|Xs],
     [split(D,(right,TP,left));L?],
     [split(D,(left,TP,right)) | R?]) :-
    odd(D) |
    tree(Xs?,L,R)@TP.
trec([split(D++,TP)|Xs],
     [split(D,(right,TP,left,forward(D1)));L?],
     [split(D,(left,TP,right,forward(D1))) | R?]) :-
    cven(D), D1:=(2^(D/2-1)) {
tree(Xo?,L,R)@TP.
tree([],[],[]).
leaf([split(_,TP)|Xs]) :- loaf_split(Xs?)CTP.
leaf([]).
leaf_oplit(Xs) :-
```

tree(Xs,L,R),
leaf(L?)@(left,forward),
leaf(R?)@(right,forward).

odd(X) :- X=\=(X/2)*2. even(X) :- X=:=(X/2)*2.

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Education

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M.Phil. in computer science, December 1981, Yale University.

M.S. in computer science, December 1980, Yale University.

B.A./B.S. in mathematics and philosophy, with distinction, June 1979, Tel Aviv University.

Current Research Interests

Theory of logic programming and applications of the programming language Prolog; algorithms for program debugging; programming environments; concurrent computing and operating systems; computational complexity; inductive inference.

Experience

- June 1982-present
 - Research Fellow, Department of Applied Mathematics, Weizmann Institute of Science.
- June 1982-present

Consultant to the Distributed AI Project, SRI International.

- November 1981-June 1982
 - Consultant to the Knowledge Based Programmer's Assistant Project, Department of Computer Science, University of Illinois at Urbana-Champaign.
- June 1980-June

Research assistant, Department of Computer Science, Yale University.

January 1980-June 1981

Teaching assistant, Department of Computer Science, Yale University.

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Programmer and systems analyst, at Dagesh-Consulting and Data Processing, Tel Aviv.

October 1973-April 1977

Service in the Israeli Defense Forces. Terminal rank and position: lieutenant, company commander.

Conference and Workshop Presentations

- First International Logic Programming Conference, Marseille, France, September 1982.
- Prolog Programming Environments Workshop, Linkoping, Sweden, March 1982.
- The Ninth ACM Symposium on Principles of Programming Languages, Albuquerque, New Mexico, January 1982.
- Machine Intelligence 10, Cleveland, Ohio, November 1981.
- NATO ASI Summer School on Program Construction, Chateau de Bonas, France, September 1981.
- Seventh International Joint Conference on Artificial Intelligence, Vancouver, B.C., Canada, August 1981.
- Workshop on Logic Programming and Intelligent Systems, RMS Queen Mary, Long Beach, California, August 1981.
- Logic Programming Workshop, Syracuse, New York, March 1981.

Seminars

Bar Ilan University; Edinburgh University; The Hebrew University at Jerusalem; Imperial College of Science and Technology; Purdue University; Technion - Israeli Institute of Technology; Tel Aviv University; University of Illinois at Urbana-Champagne; University of Texas at Austin; Weizmann Institute of Science; XEROX-PARC, Hewlett-Packard, Palo Alto; IBM San Jose; SRI International; Stanford University.

Thesis

"Algorithmic Program Debugging", Ph.D. Thesis, May 1982. Thesis advisor: Dana C. Angluin.